Completeness of Incorrectness Separation Logic by Weakest Postcondition

Yeonseok Lee (Nagoya University), Koji Nakazawa (Nagoya University)

Goal

Proving the relative completeness of Incorrectness Separation Logic [3] with infinitary formulas

- By calculation of weakest postconditions (cf. Reverse Hoare Logic (RHL) [1])

Hoare Logic (HL)

- HL checks "correctness" of programs.
 - Hoare Triples: $\{P\} \subset \{Q\}$
 - For all states s in precondition P if running $\mathbb C$ on s terminates in s', then s' is in postcondition Q
- post(\mathbb{C}, P) describes states the set of obtained by executing \mathbb{C} on P
- Q over-approximates $post(\mathbb{C}, P)$, i.e., $post(\mathbb{C}, P) \subseteq Q$

Reverse HL & Incorrectness Logic (IL) [2]

- Viewpoint opposite to HL; checks for "incorrectness"
 - ightharpoonup Triples: [P] \mathbb{C} [Q]
- For all states s' in Q, s' can be reached by running $\mathbb C$ on some s in P
- Q under-approximates $post(\mathbb{C}, P)$, i.e., $post(\mathbb{C}, P) \supseteq Q$

Incorrectness Separation Logic [3]

- Incorrectness Separation Logic = Reverse HL + Separation Logic
 - Moreover, we consider exit statuses (ok = normal end / er = error)
- $x \mapsto y$: Singleton heap
- $x \mapsto$: Negative heap (necessary for the "frame rule")
 - x has been deallocated
- P * Q (separating conjunction) : P and Q hold for disjoint portions (heaps) → modular reasoning
- Heap model
 - $s: var \rightarrow val (store): representing (dis-) equalities between$ variables
 - $h: loc \rightarrow_{fin} val (heap): describing states of heaps$

$$s,h \models P_1 * P_2 \Leftrightarrow \exists h_1,h_2.h = h_1 \circ h_2$$

$$\land s,h_1 \models P_1 \land s,h_2 \models P_2$$

$$s,h \models x \mapsto y \Leftrightarrow \text{dom}(h) = \{s(x)\} \land h(s(x)) = s(y) \neq \bot$$

$$s,h \models x \nleftrightarrow \Leftrightarrow \text{dom}(h) = \{s(x)\} \land h(s(x)) = \bot$$

$$s,h \models x \approx y \Leftrightarrow s(x) = s(y) \land \text{dom}(h) = \varnothing$$

• Inference rules ($\epsilon \in \{ \text{ok, er} \} \}$ is an exit status)

$$\begin{array}{c|c} \operatorname{Cons} & \mathsf{Reversed} \; \mathsf{with} \; \mathsf{HL!} \\ \underline{p' \Rightarrow p} & \vdash [p'] \; \mathbb{C} \; [\epsilon : q'] \qquad q \Rightarrow q' \\ & \vdash [p] \; \mathbb{C} \; [\epsilon : q] \\ \hline \mathsf{DISJ} \\ \underline{\vdash [p_1] \; \mathbb{C} \; [\epsilon : q_1]} & \vdash [p_2] \; \mathbb{C} \; [\epsilon : q_2] & \operatorname{FREE} \\ & \vdash [p_1 \lor p_2] \; \mathbb{C} \; [\epsilon : q_1 \lor q_2] & \vdash [x \mapsto e] \, \mathsf{L:free}(x) \; [\mathit{ok} : x \not\mapsto] \end{array}$$

Formulas of ISL with infinite disjunctions

$$P ::= \bigvee_{i \in I} \exists \overrightarrow{x} . \psi_i \mid \psi \qquad \qquad (I \text{ may be infinite})$$

$$\psi ::= \psi^* \psi \qquad \qquad \text{Quantifier-free symbolic heap}$$

$$\mid \text{emp} \mid x \mapsto y \mid x \not \mapsto \qquad \text{Spatial Formulas}$$

$$\mid x \approx y \mid x \not\approx y \qquad \qquad \text{Pure Formulas}$$

Relative Completeness of ISL

Theorem: For all $P, \mathbb{C}, \epsilon, Q$, if $[P] \mathbb{C} [\epsilon : Q]$ is true, then $[P] \ \mathbb{C} \ [\epsilon : Q]$ is provable.

(Outline of the proof)

- 1. Proving Expressiveness by defining weakest postcondition $\forall \sigma' . \sigma' \in WPO[[P, \mathbb{C}, \epsilon]] \iff \sigma' \models wpo(P, \mathbb{C}, \epsilon)$
 - $\bullet \ \mathsf{WPO}[\![P,\mathbb{C},\epsilon]\!] = \{\sigma' \mid \exists \sigma . \, \sigma \models P \land (\sigma,\sigma') \in [\![\mathbb{C}]\!]_{\epsilon} \}$
 - A set of states satisfying weakest postconditions for P, \mathbb{C} and ϵ
- 2. Proving that weakest postcondition is always derivable
 - For all P, \mathbb{C}, ϵ , $\vdash [P] \mathbb{C} [\epsilon : wpo(P, \mathbb{C}, \epsilon)]$

Weakest postconditions

- Case analysis for all pairs of variables $(x = y \text{ or } x \neq y)$
 - Every formula can be transstated to the form:

$$\bigvee_{i \in I} \exists \overrightarrow{x} . \psi_i$$

 (ψ_i) is a finite symbolic heap for each case, i.e.,

 $\forall i \in I, \forall y, z \in fv(\psi_i) \cup fv(\mathbb{C}) . y \approx z \models \psi_i \text{ or } y \not\approx z \models \psi_i)$

• Definition of wpo $(P, \mathbb{C}, \epsilon)$

$$\operatorname{wpo}(P, \mathbb{C}, \epsilon) = \bigvee_{i \in I} \exists \overrightarrow{x} . \operatorname{wpo}_{\operatorname{sh}}(\psi_i, \mathbb{C}, \epsilon)$$

(P is equivalent to $\sqrt{\exists \vec{x}} \cdot \psi_i$)

- ullet Definition of $\operatorname{wpo}_{\operatorname{sh}}(\psi,\mathbb{C},\epsilon)$ for (qf-)symbolic heaps ψ
 - $\bullet \ \mathsf{wpo}_{\mathsf{sh}}(\psi'^*x \, x \approx y^*y \, \mapsto \, e, \mathsf{free}(\mathsf{x}), \mathsf{ok}) = \psi'^*x \, x \approx y^*y \, \not\mapsto$ • wpo_{sh}(ψ , free(x), ok) = false (otherwise)
 - Case 1: x is a fresh address • $wpo_{sh}(\psi, x := alloc(), ok) =$ $\exists x'. (\psi[x := x'] * x \mapsto -) \lor$

Case 2: x was once allocated (x \mapsto)

- $\bigvee_{j=1}^{n} ((*_{i=1}^{n} y_i \not \mapsto)[y_j \not \mapsto := y_j \mapsto -] *_{x} \approx y_j *_{\psi}[x := x'])$ $(\psi = (*_{i=1}^n y_i \not\mapsto) * \psi'$ s.t. ψ' does not contain $\not\mapsto$)
- $wpo_{sh}(\psi, x := alloc(), er) = false$
- $\operatorname{wpo}_{\operatorname{sh}}(\psi, \mathbb{C}_1; \mathbb{C}_2, \operatorname{ok}) = \operatorname{wpo}(\operatorname{wpo}_{\operatorname{sh}}(\psi, \mathbb{C}_1, \operatorname{ok}), \mathbb{C}_2, \operatorname{ok})$
- $\operatorname{wpo}_{\operatorname{sh}}(\psi, \mathbb{C}_1; \mathbb{C}_2, \operatorname{er}) =$ $\operatorname{wpo}_{\operatorname{sh}}(\psi,\mathbb{C}_1,\operatorname{er})\vee\operatorname{wpo}(\operatorname{wpo}_{\operatorname{sh}}(\psi,\mathbb{C}_1,\operatorname{ok}),\mathbb{C}_2,\operatorname{er})$ $\mathsf{wpo}_{\mathsf{sh}}(\psi, \mathbb{C}^{\star}, \mathsf{ok}) = \bigvee \Upsilon(n)$

$$n \in \mathbb{N}$$

 $(\Upsilon(0) = \psi \text{ and } \Upsilon(n+1) = \text{wpo}(\Upsilon(n), \mathbb{C}, \text{ok}))$

References

- [1] De Vries, E., Koutavas, V.: Reverse hoare logic. SEFM 2011, 155–171.
- [2] O'Hearn, P.W.: Incorrectness logic. POPL 2019, 1–32.
- [3] Raad, A., Berdine, J., Dang, H.H., Dreyer, D., O'Hearn, P., Villard, J.: Local reasoning about the presence of bugs: Incorrectness separation logic. CAV 2020, 225–252.